



Cryptographic Hardware and Secure Elements

A security architect's view

Secure Application Development

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- British born and educated, living in Brazil since 1997
- Masters Degree ('92) in Information Engineering from Southampton University
- 20+ years experience in design and development of systems and secure applications, based on technologies such as smart-cards and secure elements.
- Author of "Criptografia Essencial a Jornada do Criptógrafo" – Elsevier 2016.



Smart Card Technology

ORIGINS AND EVOLUTION



Origins

1968 Patent – Plastic cards with microchips Jürgen Dethloff / Helmut Gröttrup

1974 Patent on chip Cards Roland Moreno – 'Father' of smart-cards

1978 Patent on "Self Programmable one-chip MPU" Michel Ugon - Bull

1979 First Production Cards Bull, Motorola

Note: Bibliography diverges on exact details and attribution



Evolution

~1980 Memory Cards

Hardwired logic, EPROM, E²PROM memory

Microprocessor Cards

Secure MCU, Crypto HW, Monolithic OS

Multi-Application Cards

VM + Applets, App-Firewalls, Remote App Management

Near Field Communication & Secure Elements NFC, Mobile (SE) and Cloud (HCE)

2018+ IoT & Consumer

Embedded SE, integrated, emerging technologies ...

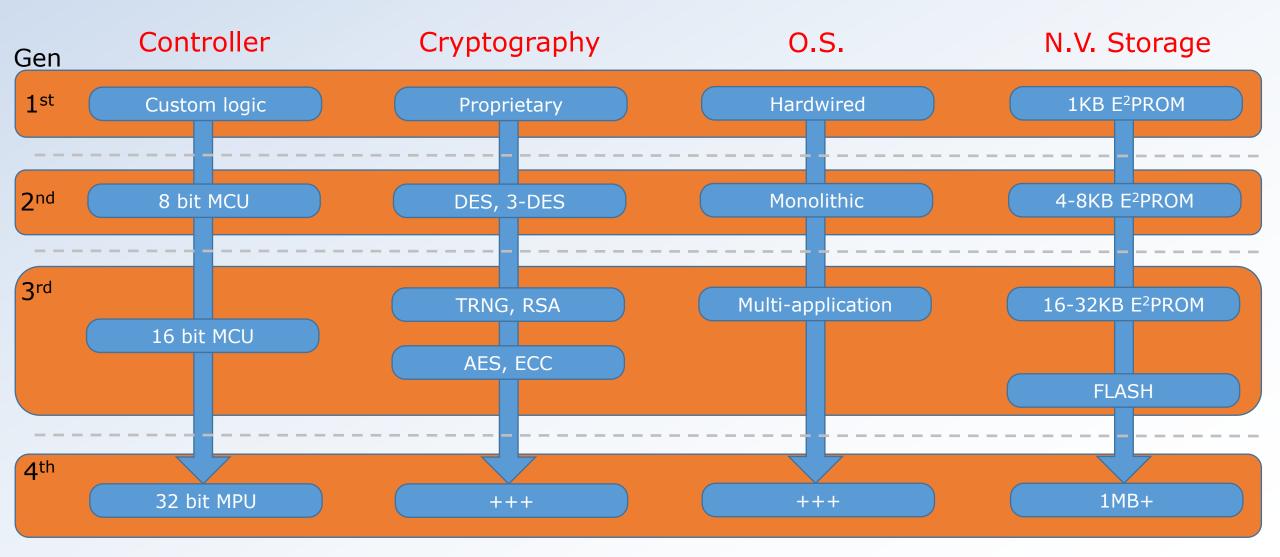


Evolving HW and SW Features



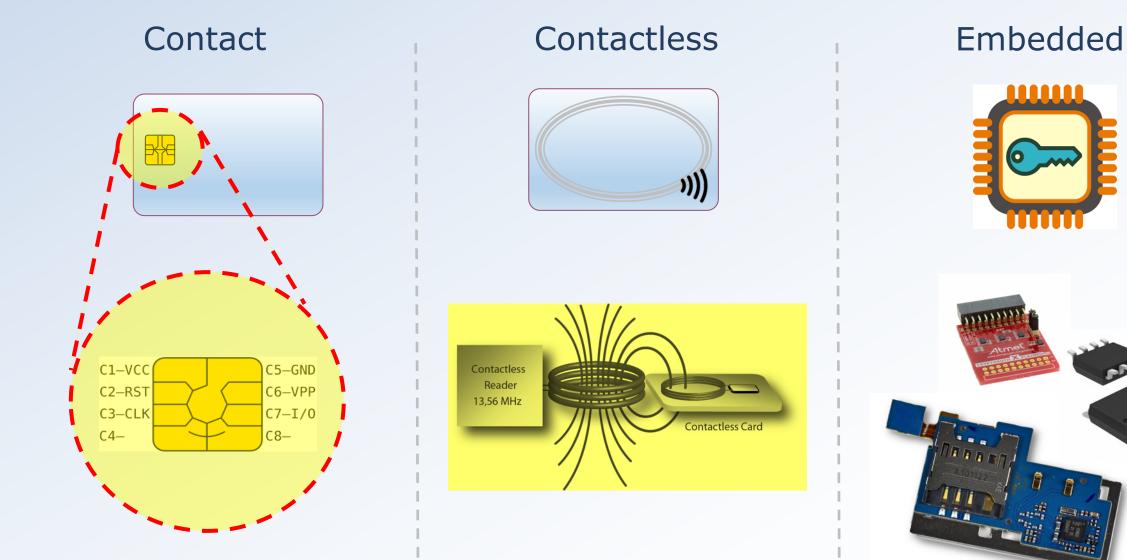


Evolving HW and SW Features



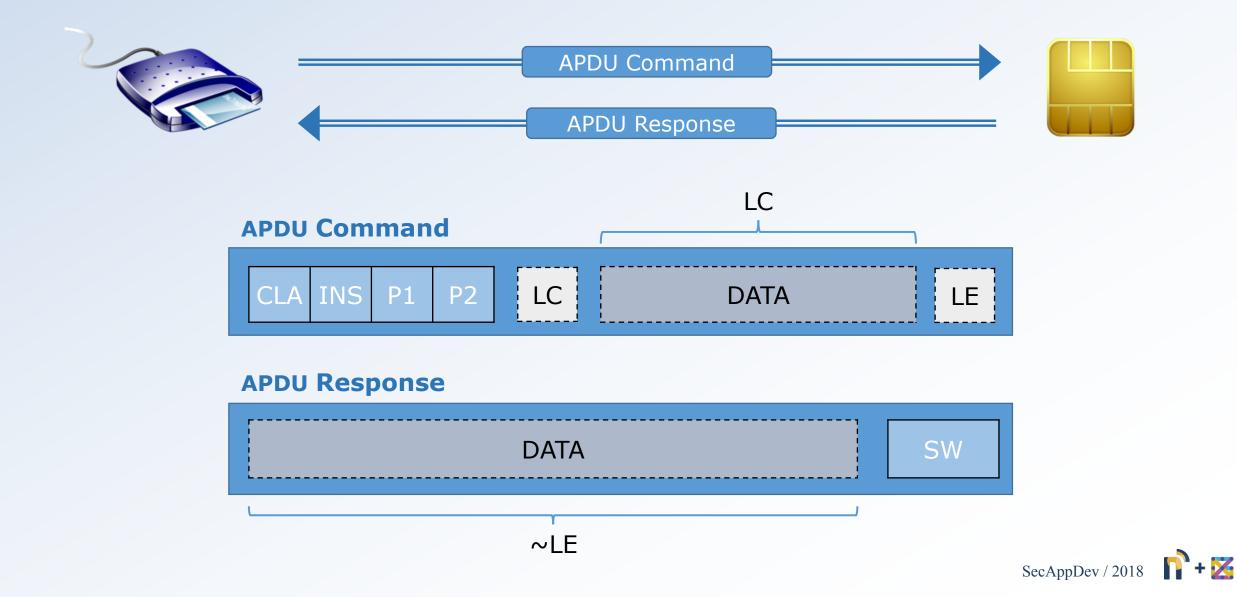


Communication Interfaces

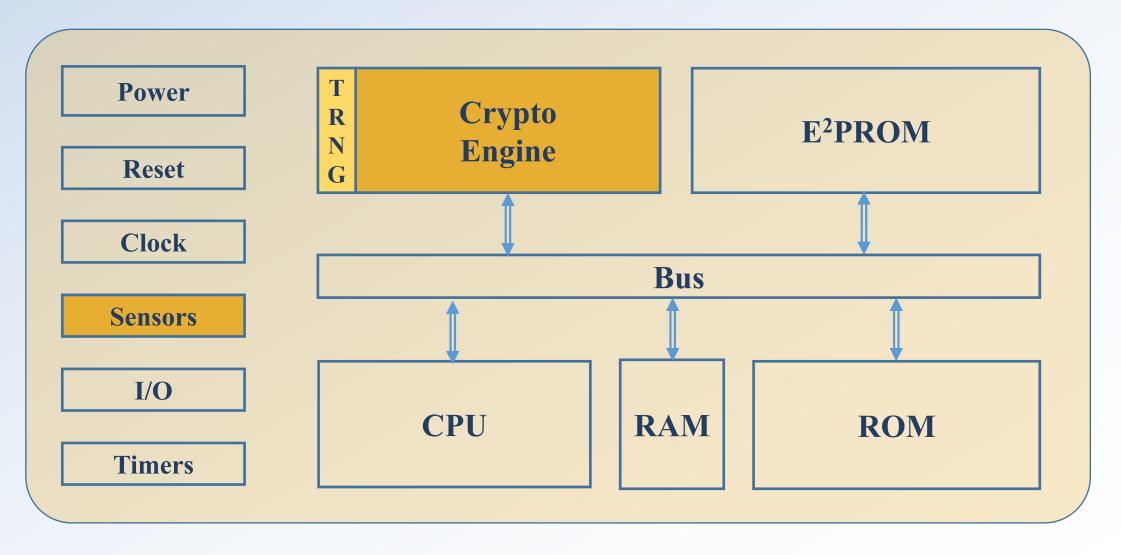


SecAppDev / 2018 👖 + 🔀

Communication Flow



Typical Secure MCU Architecture





Tamper and Side-Channel Resistance



Inbuilt defenses against:

Physical (Invasive) Attacks

De-packaging, Micro-probing, rev-Engineering,Scanning Electron Microscopy, ...

Active and Environmental Attacks

Temperature, Voltage Glitches, Laser, Clock, Reset, ...

Passive Monitoring

Current Consumption, Electromagnetic Emissions, Timing





ACCESS CONTROL SYSTEM FOR INDUSTRIAL PREMISES CIRCA 1998



Project Brief

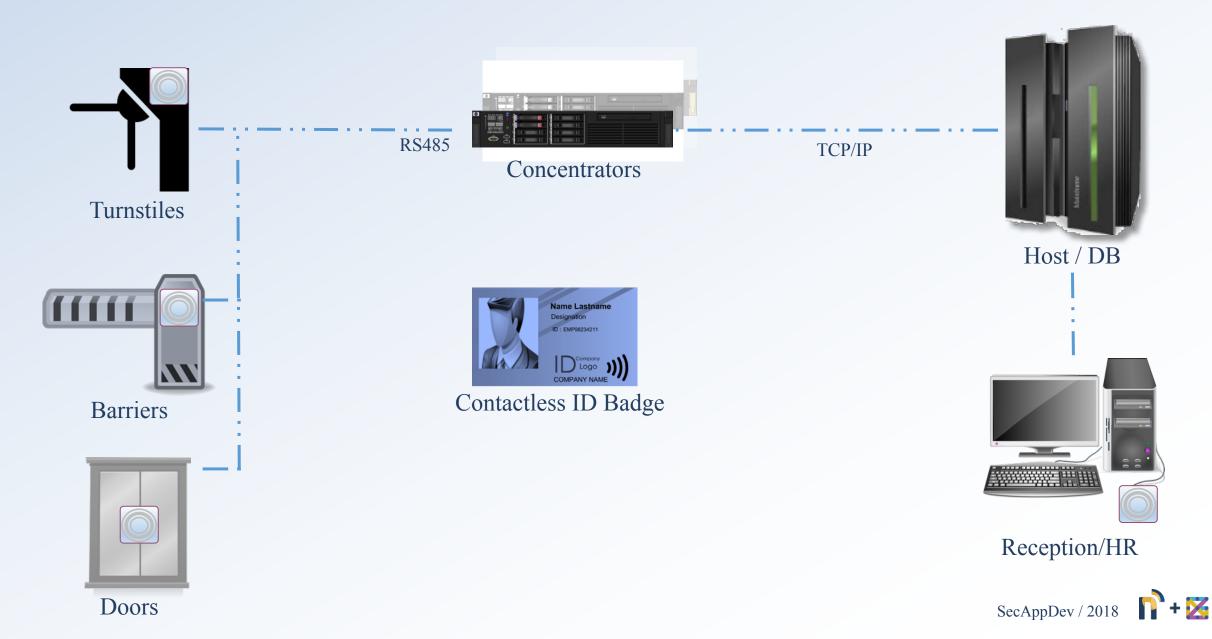
Project Employee / Contractor ID and Access Control System

Location Steelworks, in Volta Redonda (~6km²)

Contactless ID Badges Using MIFARE "Classic" cards (~40,000)

Robust Hostile Industrial Environment Fault-tolerant distributed system architecture

System Architecture



Contactless ID Badges



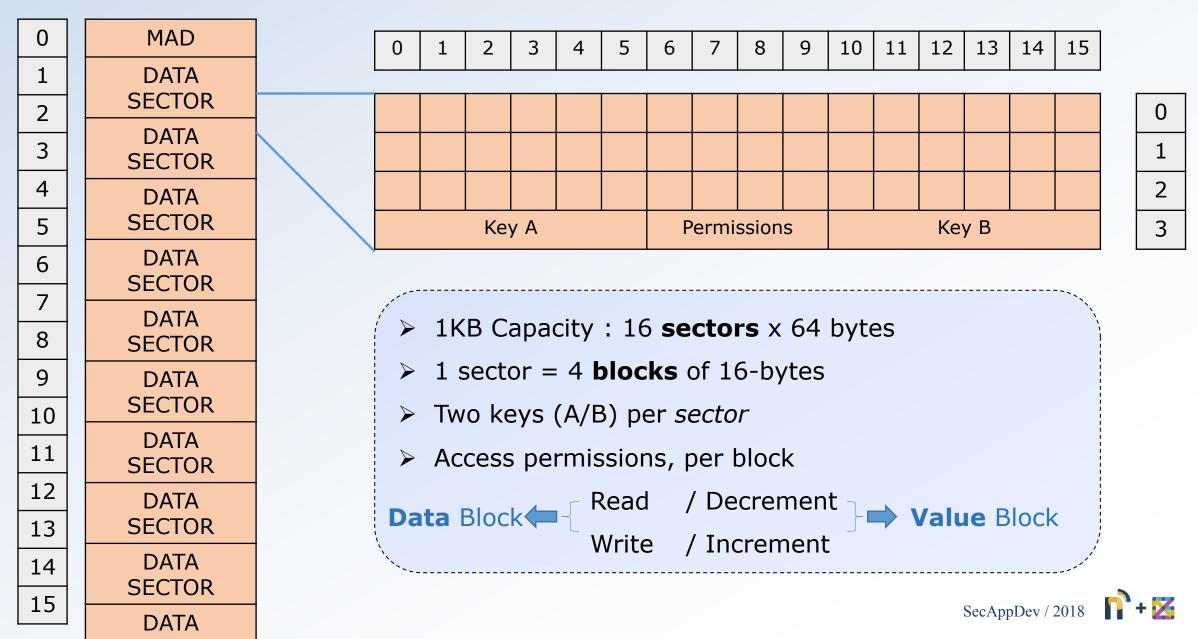
MIFARE Classic Contactless Smartcards

Features: ✓ *State of the Art* in 1998

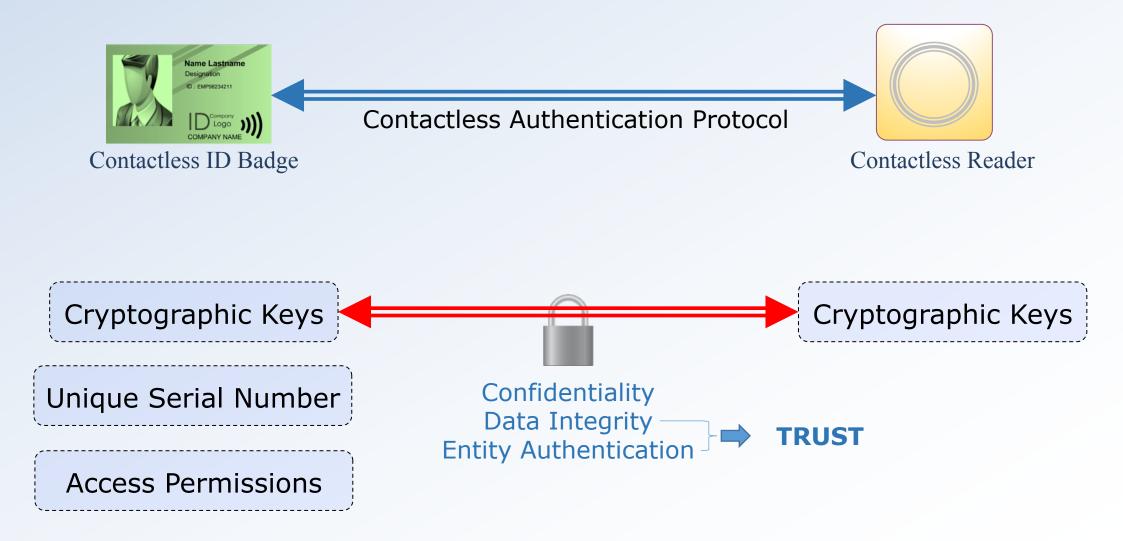
- $\checkmark\,$ 1KB Capacity, divided in sectors and blocks
- ✓ Cryptographic authentication, based on proprietary CRYPTO-1 cipher (48 bits)
- ✓ On-chip Unique Identifier



MIFARE Classic Layout

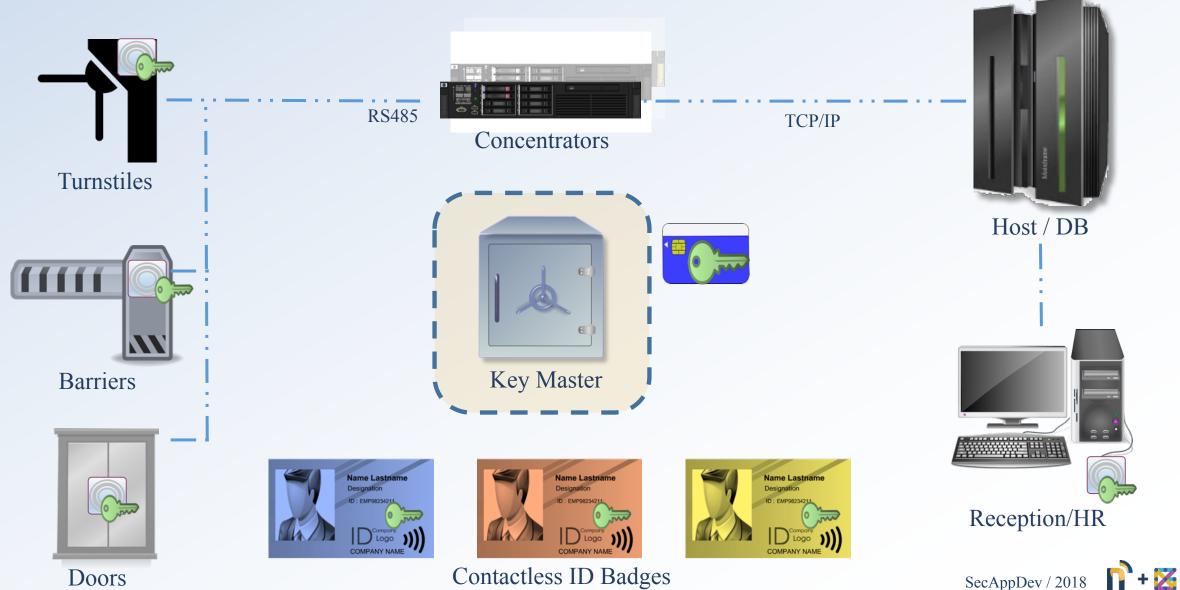


MIFARE Classic Security Properties



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Key Generation, Storage and Provisioning



Doors

IN 1998,

THIS COMMERCIAL SYSTEM'S SECURITY LEVEL WAS CONSIDERED OPTIMUM

GIVEN THE AVAILABLE TECHNOLOGY





CONTACTLESS TICKETING SYSTEM

CIRCA 2008



Project Brief

Project Electronic Ticketing System for inter-municipal busses

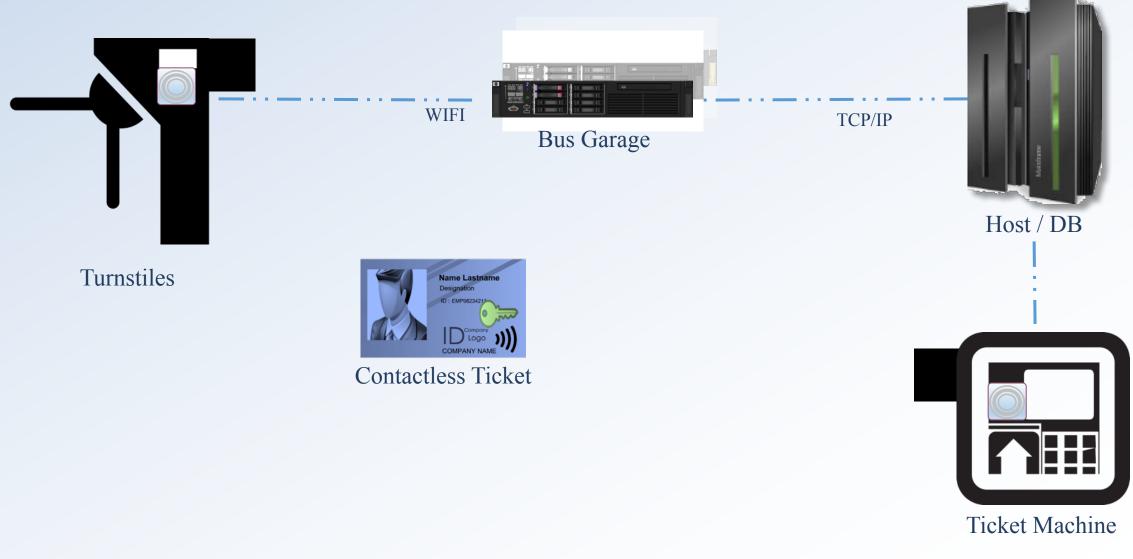
Localization Southern Brazil

Contactless Tickets Using same MIFARE "Classic" cards

Security Requirements Anti-fraud, anti-cloning protection Fault-tolerant distributed system architecture



System Architecture



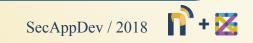


IN 2008, CAN WE USE A SECURITY SOLUTION FROM 1998





We LEARN to build BETTER SECURITY



Security Evaluation of 1998 Solution



X SYSTEM-WIDE KEY – in all cards and readers

Card Data/Permissions may be ALTERED

X Newer Cards have **Programmable ID**

Transaction Logs may be ADULTERATED

X Proprietary CRYPTO-1 Cipher rev. Engineered

Cards may be CLONED or "RESTORED"



HOW CAN SECURITY BE IMPROVED

USING THE SAME LOW-COST CARDS



Introducing the Secure Element

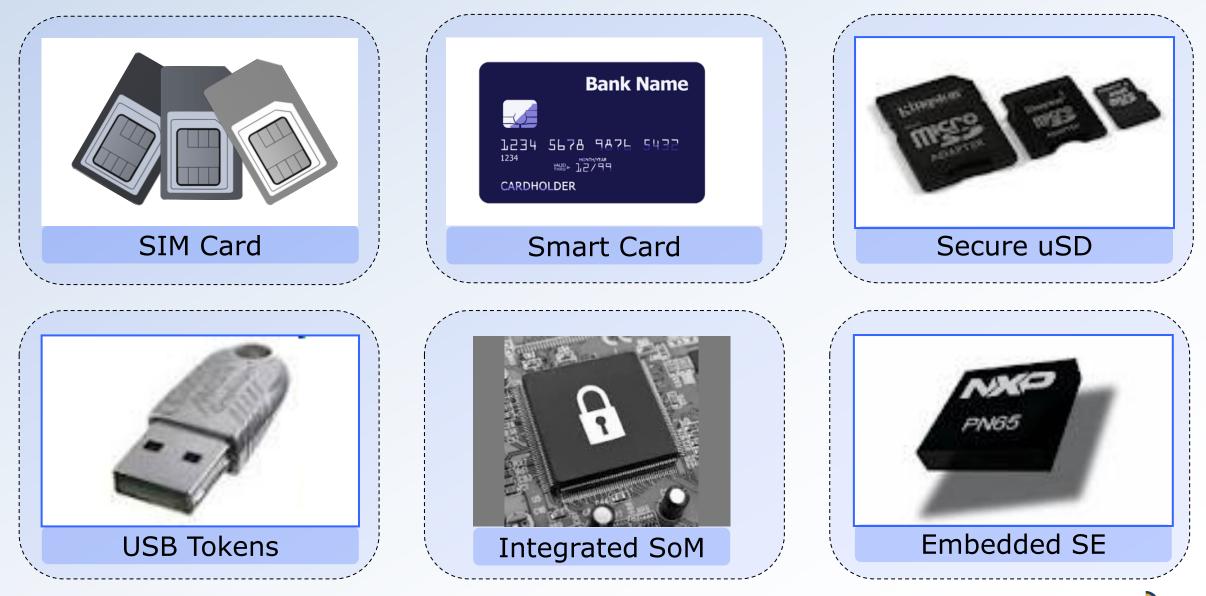


Eurosmart:

"A SECURE ELEMENT contains a certified microcontroller and embedded software. It is secure, personal and portable and comes in multiple form factors : smart card, USB token, microSD, etc. ...{snip}... Secure elements have a strategic role in protecting digital identities and are vital to ensure digital security and privacy."



Many different Form Factors



Another Definition



Global Platform:

"A <u>Secure Element (SE) is a tamper-resistant</u> platform, typically a one chip <u>secure microcontroller</u>, capable of <u>securely</u> hosting <u>applications</u> and their <u>confidential and</u> <u>cryptographic data</u> (e.g. key management) in accordance with the rules and <u>security requirements</u> set forth by a set of well-identified <u>trusted authorities</u>."



Characteristics of Secure Elements



Isolated Execution Sandboxes



Clearly Defined Access Controls

Minimized TCB - Trusted Code Base

Secure Cryptographic Service Stack

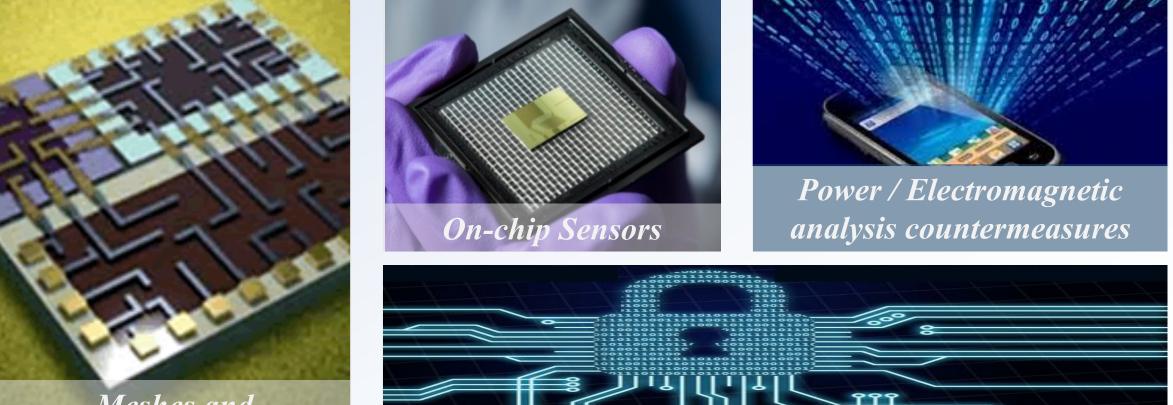


Protected Remote Update Mechanisms



Tamper Resistance

Secure Elements include multiple protection mechanisms against physical, active and passive attacks, including SCA & FI.

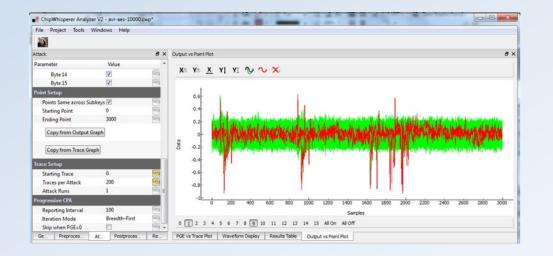


Meshes and Encrypted Busses

Proprietary and Patented Techniques



Side Channel Attacks



Side Channels

"A side-channel leaks information as a result of some physical, electrical or other behavioural characteristic of a system, that can be measured"

Execution Speed Current Consumption Current Leakage Electromagnetic Emissions



Fault Injection Attacks



Fault Injection

"Perturbation of an execution environment with the sole objective of provoking a specific failure, in a controlled manner, within software or electronic circuits."

Voltage Clock Glitching Electromagnetic Pulses

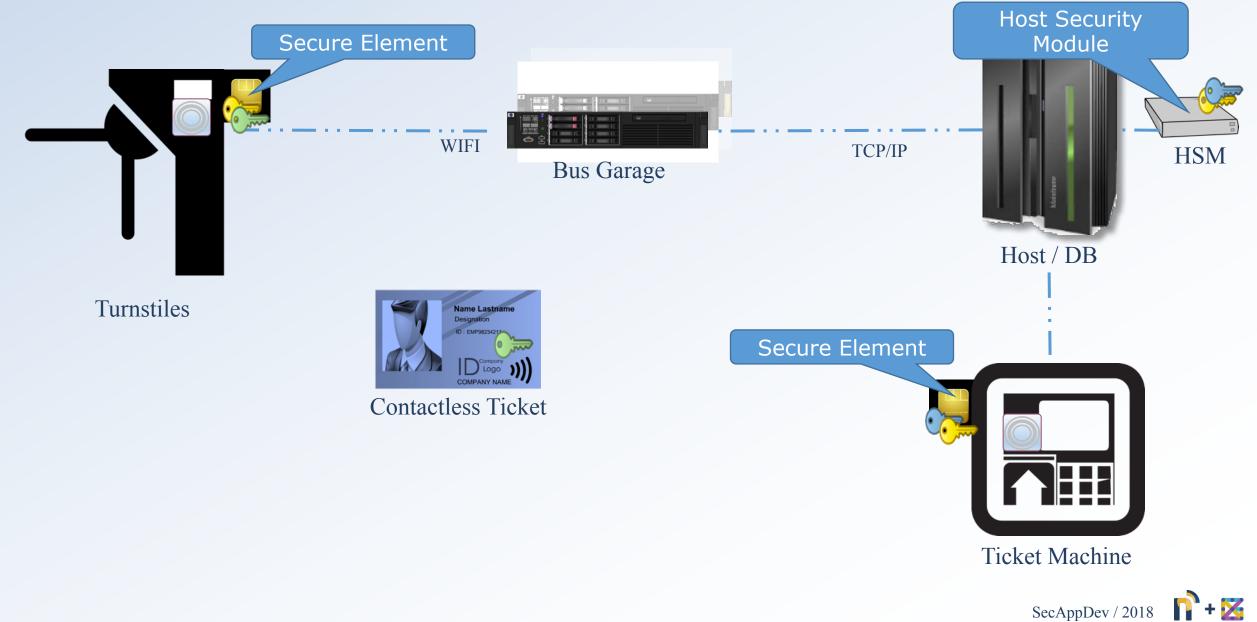




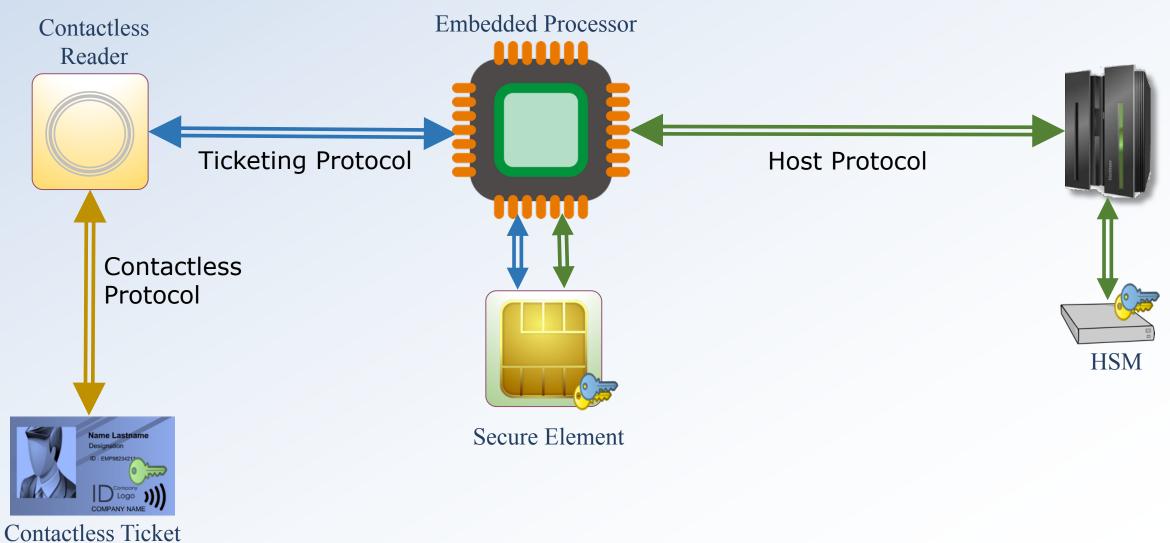
CONTACTLESS TICKETING SYSTEM USING Secure Elements



New Architecture

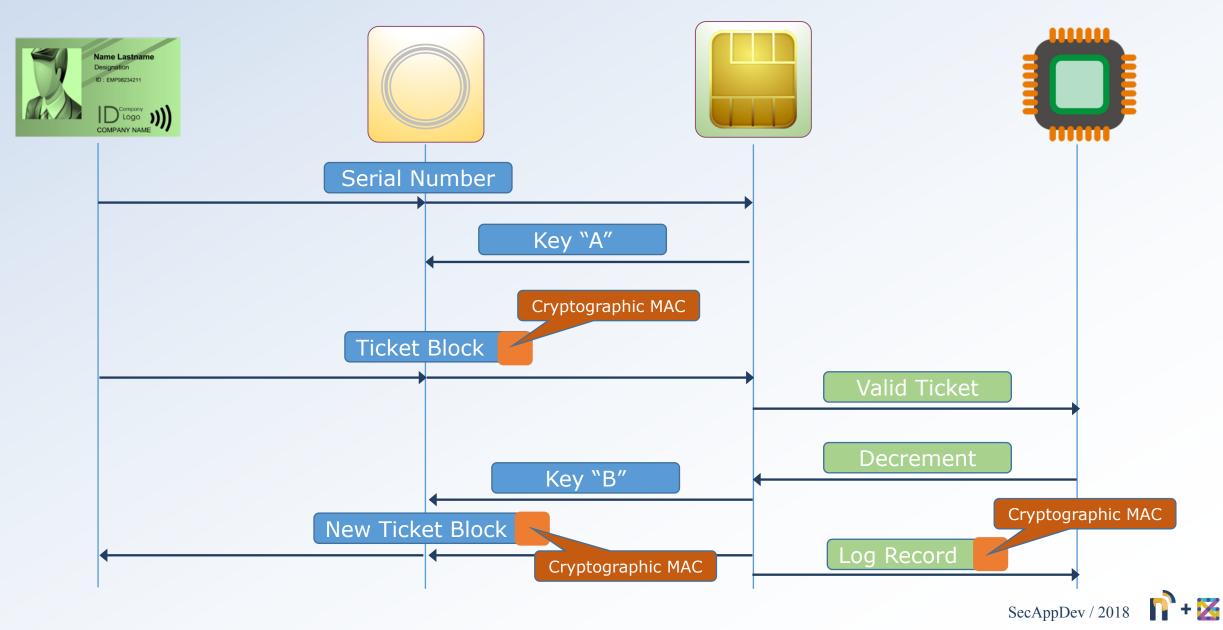


Information Flow





Example of Secure Decrement



Security Evaluation



✓ Serial Number and Master Keys used to <u>derive</u> set of UNIQUE <u>per card</u> keys

✓ Cryptographic SIGNATURES protect

✓ Ticket <u>Values</u>

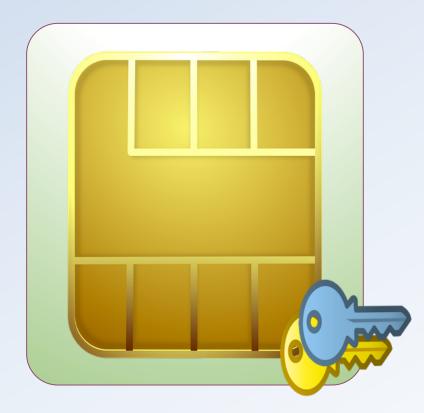
✓ Log <u>Records</u>

✓ Host <u>Commands</u>

✓ All <u>Keys</u> and <u>Cryptographic Operations</u> PROTECTED by Secure Element



Properties of the Secure Elements



- ✓ Acts as *Hardware* Root of Trust
- ✓ Secure Container for Keys and other
 Critical Data
- ✓ **Secure Execution Environment** for stack
 - of "High Level" Secure Services
- **√UNIQUE** keys for <u>Mutual HOST</u>

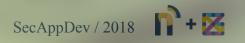
Authentication



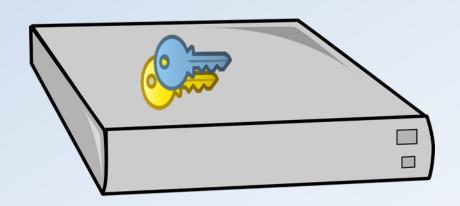
DID SOMEBODY SAY

H.S.M.

HOST/HARDWARE SECURITY MODULE



HSM – Hardware Security



✓ High-performance Cryptographic

Hardware for Servers

✓ <u>Secure Key Storage</u> – plaintext Keys

never leave HSM

✓ Modern HSM's offer a <u>Secure Execution</u>

Environment for stacks of "High Level"

Secure Services

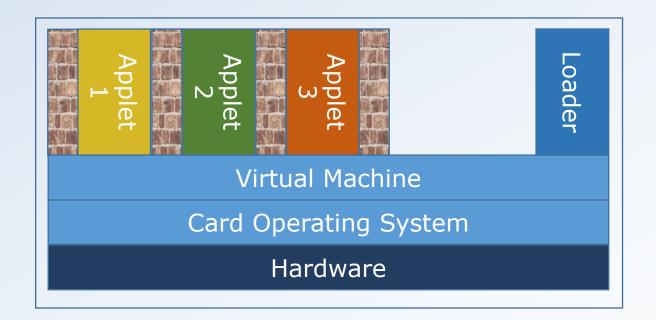


Secure Elements

IN A MODERN WORLD



Multi-Application Paradigm



- Multiple independent card-resident applications (applets)
- Highly specialized Virtual Machine executes applet code
- ✓ **Firewalls** enforce Applet separation

- Isolation between applets and Card Operating System
- Well-specified API for comms, crypto, storage, state management
- Remote Applet Management



Principal Alternatives



MULTOS

- "Open" Consortium MAOSCO
- > Develop in \underline{C} / ASM
- ➢ Markets: Banking, e-ID, IoT
- Centralized KMA using RSA certificates, or Issuer-centric (step-one)



JavaCard

- Sun Microsystems, now Oracle
- Develop in Java (OOP)
- Markets: SIM-Cards, Banking, e-ID
- Issuer-centric management



Development Process



SmartDeck Suite

Write code in C (and/or ASM if desired)

- Reduced libc, no dynamic mem.
- Libraries of Primitives
- Global data-spaces (NV, private, public)

Compile, then ...

- Generate ALU Application Load Unit
- Generate/Request ALC Load Certificate
- Emulate, or Load and test



JavaCard Dev.

Write code in Java

- Lots of missing types and classes
- Use javacard.* namespaces
- Static objects, *fixed* mem. usage

Compile, then ...

- Java byte-card conversion
- > Off-card byte-code verifier
- Emulate, or Load and test



Embedded (resource-constrained) *mindset* and **card-specific** functionality Non-volatile Memory, Communication, Cryptography, Atomic Transactions, ...



NFC MOBILE PAYMENTS USING VIRTUAL SECURE ELEMENTS 2018



NFC



NFC (Near Field Communication) Forum created in 2004



Types of TAG

Operating Modes

Reader / Write Mode

Device can read/write any supported TAG type



NFC

NFC

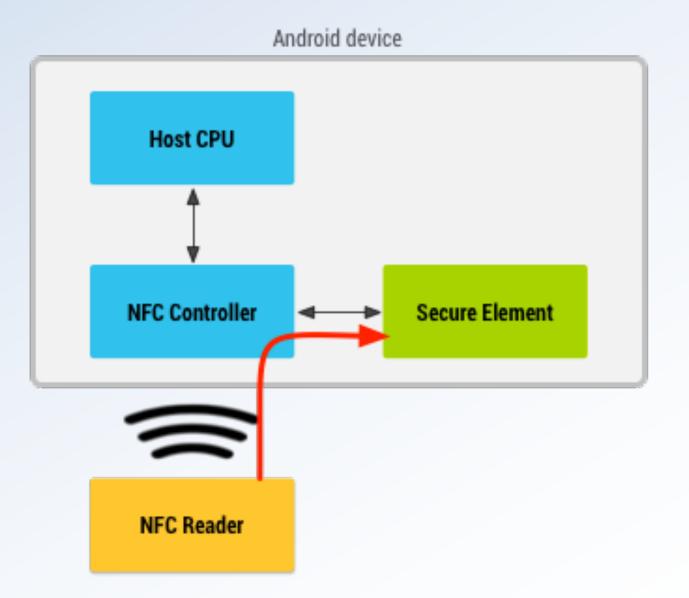
Card Emulation Mode Device acts as a contactless

Peer to Peer Mode Two NFC devices can exchange data





NFC-style Secure Elements



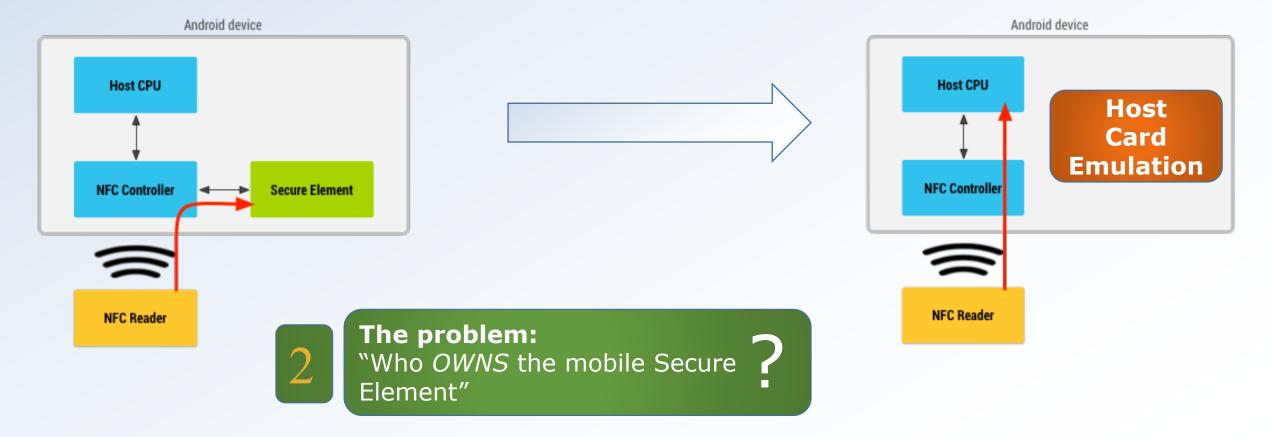


From NFC to HCE, in 3 easy steps ...

1

Developer says: "I want to load my APPLET onto the phone's Secure Element"

Google's solution: "Forget about the Secure Element, we'll do it in software!"





Characteristics of HCE



Uses Standard Android Sandboxes

APP processes APDU commands



Delegate security to Server?



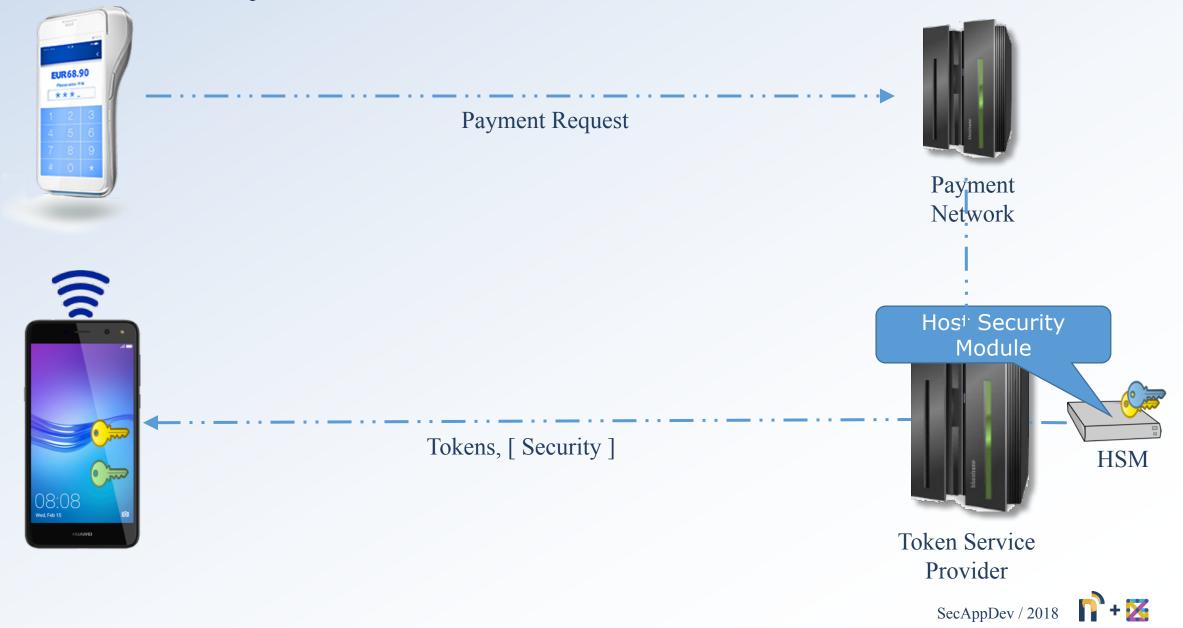
Use Android HW-Backed Keystore?



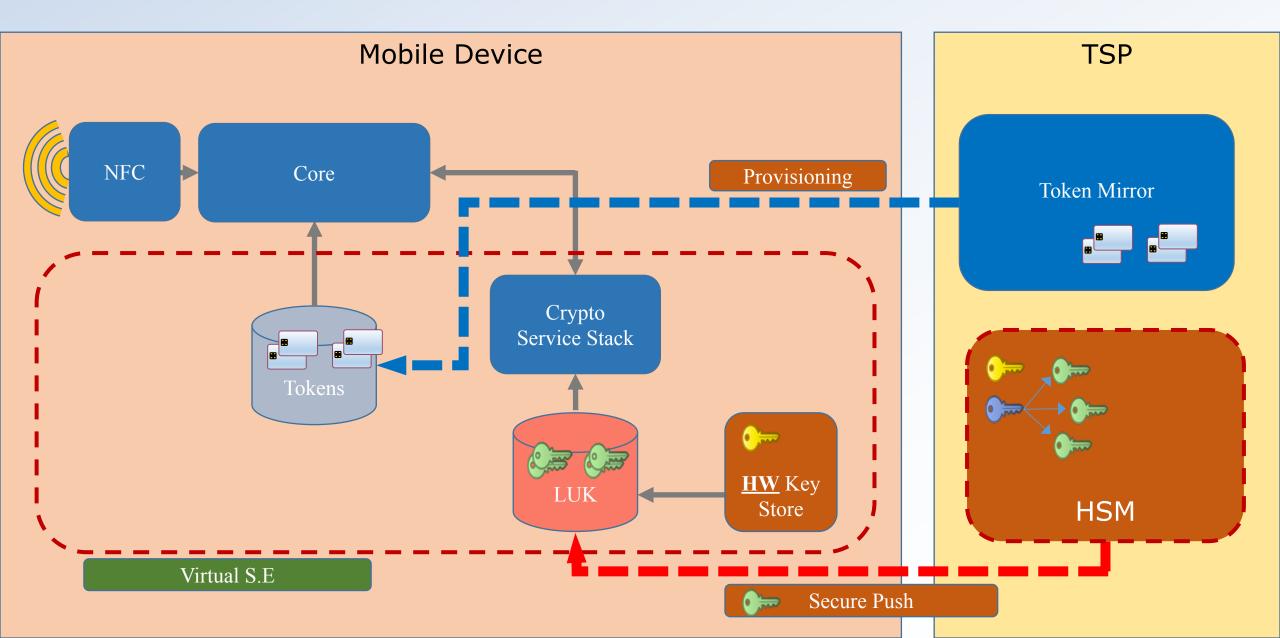
Root/Debug/Tools can be a problem



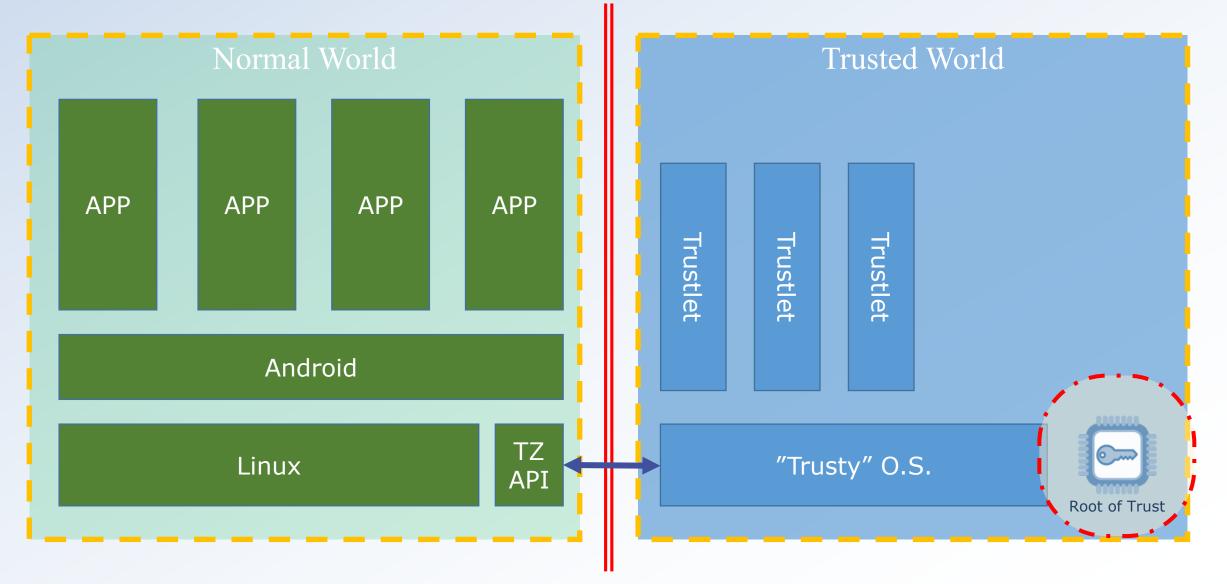
Payment Token Service Provider



Virtual Secure Elements



Android/TEE Architecture



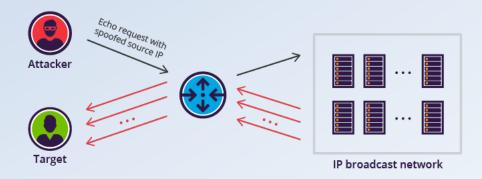


Secure Elements For THE IOT

A PROMISING FUTURE IN THE MAKING ...



IoT : Security Recommendations**



✓ Secure by Design

✓ Hardened Configurations

✓ Secure Updates

✓ Unique Credentials per Device

✓Cryptography

** BITAG Internet of Things Security and Privacy Recommendations NOV/2016



Secure Elements for the IoT

MICROCHIP

High end security controller

up to 3 KB user memory

Turnkey solution

12C interface

Keyfeatures

Chptographic support: ECC156, SHA256

rousuntur pernete is A summer ranges PG-USON-10-2 Package [3×3 mm]

Full system integration support

50:37 [2] *1) *

Cinfineon

nation integrated.

Hardware root of trust with Google Cloud IoT

Google Cloud Platform

D 00:00

Core and Microchip

Join us: Tuesday, February 6th

Presented by: Antony Passemard, Google Cloud for Product Management Lead Nicolas Schiell, Sr. Strategie Lead

ALL

ChipDN

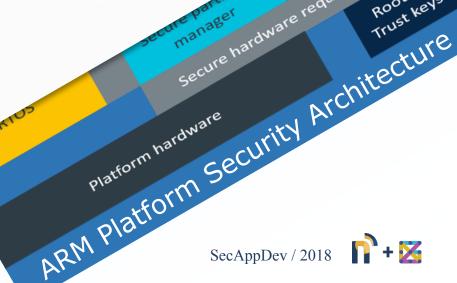
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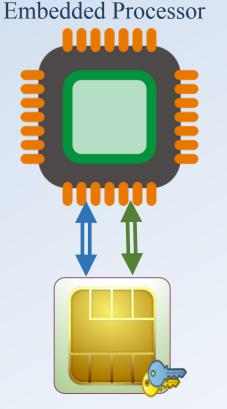


Secure hardware requirements

Boot firmware

> Root of Trust Keys

Remaining Problems ?



Secure Element

X Interception/Spoofing of communications

X Compromise of Embedded Software

X Transfer Secure Element to other machine







Hartelijk bedankt !





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https:// cryptographix.org/explore



in /in/seanwykes

